2-input NOR Gate

HITACHI

ADE-205-326C (Z) 4th. Edition April 2000

Description

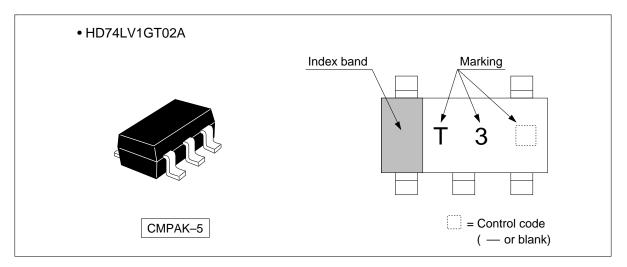
The HD74LV1GT02A is high speed CMOS two input NOR gate using silicon gate CMOS process. With CMOS low power dissipation, it provides high speed equivalent to LS–TTL series. The internal circuit of three stages construction with buffer provides wide noise margin and stable output. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as hitachi uni logic series.
- Supplied on emboss taping for high speed automatic mounting.
- TTL compatible input level. Supply voltage range : 4.5 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_0 (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current $\pm 12 \text{ mA}$ (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.



Outline and Article Indication



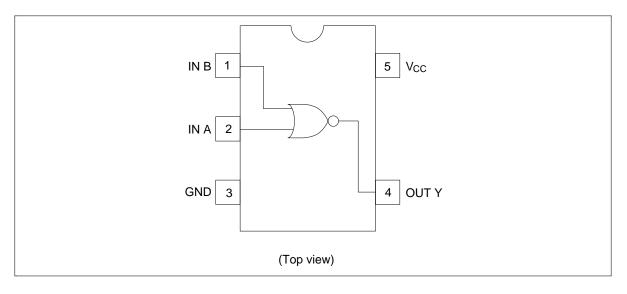
Function Table

Inputs		Output Y	
Α	В		
L	L	Н	
L	Н	L	
Н	L	L	
Н	Н	L	

H : High level

L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage	V _{cc}	-0.5 to 7.0	V	
Input voltage	V _{IN}	-0.5 to 7.0	V	
Output voltage	V _{OUT}	–0.5 to V _{cc} + 0.5	V	Output : H or L
		-0.5 to 7.0		V _{cc} : OFF
Input diode current	I _{IK}	-20	mA	
Output diode current	I _{ok}	±50	mA	
Output current	I _{OUT}	±25	mA	
V _{cc} , GND current	I _{cc} or I _{GND}	±50	mA	
Power dissipation	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Recommended Operating Conditions

Item	Symbol	Ratings	Unit
Supply voltage	V _{cc}	4.5 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{cc}	V
Operating temperature	T _{opr}	-40 to +85	°C
Input rise / fall time	t _r , t _f	0 to 20 (V _{cc} = 4.5 to 5.5 V)	ns

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}C$

Item	Symbol	V _{cc} (V) *	Min	Тур	Max	Unit	Test condition
Input voltage	V _{IH}	4.5 to 5.5	2.0		_	V	
	V _{IL}	4.5 to 5.5	_		0.8	_	
Hysteresis voltage	V _H	5.0	_	0.15	_	V	$V_T^+ - V_T^-$
Output voltage	V _{OH}	Min to Max	V _{cc} –0.1			V	I _{OH} = -50 μA
		4.5	3.8			_	I _{OH} = -12 mA
	V _{ol}	Min to Max	_	_	0.1		I _{oL} = 50 μA
		4.5			0.55	_	I _{oL} = 12 mA
Input current	I _{IN}	0 to 5.5			±1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I _{cc}	5.5	—	—	10	μA	$V_{IN} = V_{CC}$ or GND, $I_{O} = 0$
	ΔI_{cc}	5.5	—	_	1.5	mA	One input $V_{IN} = 3.4 V$, other input V_{CC} or GND
Output leakage current	I _{OFF}	0	_	_	5	μA	V _o = 5.5 V
Input capacitance	CIN	5.0		2.5		pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

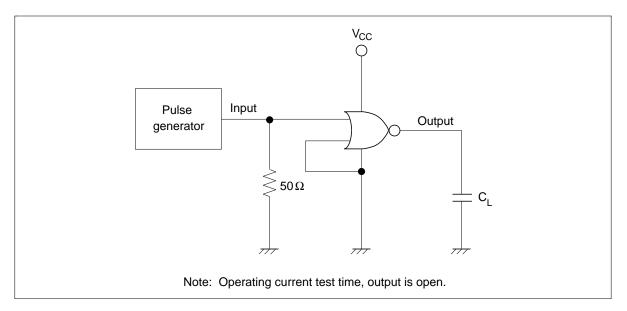
ltem	Symbol	$T_a = 2$	$= 25^{\circ}C$ $T_a = -40 \text{ to } 85^{\circ}C$		Unit	Test	FROM	то		
		Min	Тур	Max	Min	Max	-	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.0	6.2	1.0	7.5	ns	$C_{L} = 15 \text{ pF}$	A or B	Y
delay time	t _{PHL}	—	6.5	9.0	1.0	10.0	_	C _L = 50 pF	_	

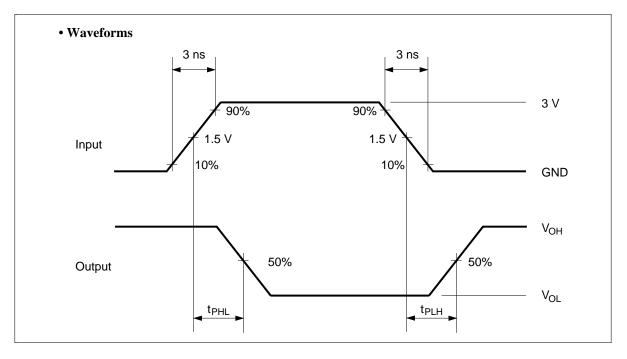
Operating Characteristics

• $C_L = 50 \ pF$

ltem	Symbol	V _{cc} (V)	$T_a = 25^{\circ}C$			Unit	Test Conditions	
			Min	Тур	Max			
Power dissipation capacitance	C_{PD}	5.0	_	10.3	—	pF	f = 10 MHz	

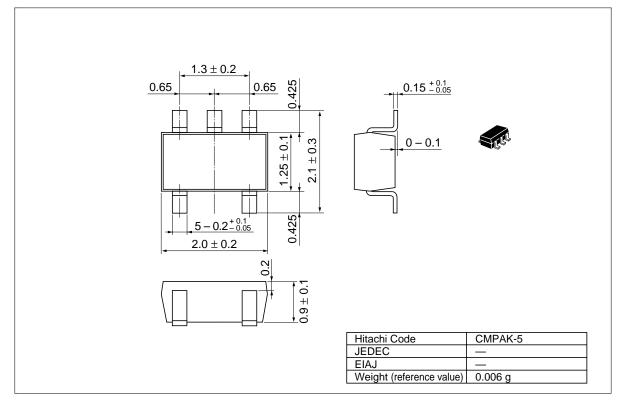
Test Circuit





Package Dimensions

Unit : mm



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